EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L30	59	((partially partly) near3 (enabl$3 disabl$3)) with port	US-PGPUB; USPAT	OR	ON	2010/01/02 17:55
L29	1	L27 and L28	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/01/02 17:54
L28	6	direct near3 loopback near3 test $3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/01/02 17:54
L27	99	((test$3 JTAG) near3 port) same (port near3 access near3 circuitry)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/01/02 17:54
L26	4	L24 and L25	USPAT	OR	ON	2010/01/02 17:54
L25	273	(inhibit near3 bit) with (one near3 time near3 programmable register)	USPAT	OR	ON	2010/01/02 17:54
L24	2102	(IC (integrated circuit)) near5 ((test $3 jtag (joint near3 action)) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/01/02 17:54
L23	26	L21 and L22	USPAT	OR	ON	2010/01/02 17:54
L22	1232	((test$3 JTAG) near3 port) same enabl$4 desabl$4	USPAT	OR	ON	2010/01/02 17:54
L21	9750	(inhibit near3 (bit nand gate circuitry))	USPAT	OR	ON	2010/01/02 17:54
L20	41	19 and (JTAG (joint adj test adj action adj group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/01/02 17:53

L19	4528	726/25-27.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/01/02 17:53
L18	6	(disabl$3 near4 (testing near3 port)) and ((den$3 block$3 enabl$3) near3 access near3 memory)	US-PGPUB; USPAT	OR	ON	2010/01/02 17:52
L17	0	(disabl$3 near4 (testing near3 port)) same ((den$3 block $3 enabl$3) near3 access near3 memory)	US-PGPUB; USPAT	OR	ON	2010/01/02 17:52

1/2/2010 5:55:51 PM
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